Code No.: AD504PC

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H.T.No.

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CMR ENGINEERING COLLEGE: : HYDERABAD UGC AUTONOMOUS

III-B.TECH-I-Semester End Examinations (Supply) - June- 2024 AUTOMATA AND COMPILER DESIGN (AI&DS)

[Time: 3 Hours]

[Max. Marks: 70]

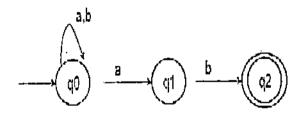
Note: This question paper contains two parts A and B.

Part A is compulsory which carries 20 marks. Answer all questions in Part A.

Part B consists of 5 Units. Answer any one full question from each unit. Each question

carries 10 marks.

	PART-A	(20 Marks)
l. a)	Define Alphabet, String and a Language in finite automata.	[2M]
b)	Differentiate Moore Machine with Melay Machine.	[2M]
c)	What is the role of Lexical Analyzer in Compiler?	[2M]
d)	Define lexeme, token and pattern.	[2M]
e)	List out the rules for FOLLOW set.	, [2M]
f)	Differentiate S-attributed and L-attributed grammars.	[2M]
	What are the rules to determine the leaders of basic blocks?	[2M]
g) h)	What is the use of algebraic transformation?	[2M]
•	Discuss relocatable machine code?	[2M]
i)	Define address descriptor and discuss its importance.	[2M]
(į	Define address descriptor and disease its importance.	
	PART-B	(50 Marks)
2	Convert the following NFA to DFA.	[10M]



3. Construct Mealy machine corresponding to Moore machine?

[10M]

States	Next States		Output
(Q)	1/P=0	I/P=1	T Output
<u>-</u> ->q1	q1	<u>q2</u>	0
ς(2	q1	q3	0
,d3	qI	q3	1

4. Construct an equivalent FA for the given regular expression (0+1)*(00+11)(0+1)*

Construct Finite Automata for the regular Expression 1(01+10)*00.

[10M]

[10M]

·	,
Construct a LAUR Parser for the Grammar:	[10M]
	-
<u>. </u>	
C-→ c/d	
OR	fc) 43
Explain in brief about Type checking and Type Conversion.	[5M]
Explain about the procedure for checking polymorphic functions.	[5M]
	[10M]
Define Basic block? Explain in brief about optimization of basic blocks.	
	[5M]
	[5M]
What are then advantages:	
•	
analysis:	
Define a Directed Acyclic Graph. Construct a DAG and write the sequence of	[10M]
instructions for the expression $a + a * (b-c) + (b-c) * d$.	
OR	
Explain the code generation process involving the environment of the code generator.	[10M]
machine registers are available.	
	Explain in brief about Type checking and Type Conversion. Explain about the procedure for checking polymorphic functions. Define Basic block? Explain in brief about optimization of basic blocks. OR Differentiate the machine dependent and machine independent code optimization? What are their advantages? How to represent the dummy blocks with no statements indicated in global dataflow analysis? Define a Directed Acyclic Graph. Construct a DAG and write the sequence of instructions for the expression a + a * (b-c) + (b-c) * d. OR Explain the code generation process involving the environment of the code generator. Explain the steps in code generation of the expression (A + B) / C + D. Assuming two

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-and-g