Code No.: AD305PC | R20 | H.T.No. | | | 8 | R | | | | | |

CMR ENGINEERING COLLEGE: : HYDERABAD
UGC AUTONOMOUS
II–B.TECH–I–Semester End Examinations (Supply) - February- 2024
COMPUTER ORGANIZATION AND MICROPROCESSOR
(AI&DS)

[Time: 3 Hours] [Max. Marks: 70]

Note: This question paper contains two parts A and B.
Part A is compulsory which carries 20 marks. Answer all questions in Part A.
Part B consists of 5 Units. Answer any one full question from each unit. Each question carries 10 marks and may have a, b, c as sub questions.

PART-A **(20 Marks)**

1. a)	Define Control Memory.	[2M]
b)	Define Digital Computer.	[2M]
c)	What are the predefined interrupts in 8086?	[2M]
d)	List the various addressing modes present in 8086.	[2M]
e)	What is the function of DD directive in 8086?	[2M]
f)	Define Interrupt programming.	[2M]
g)	Define Priority interrupt.	[2M]
h)	Define Peripheral device.	[2M]
i)	Define Pipelining.	[2M]
j)	Define Parallel Processing.	[2M]

PART-B **(50 Marks)**

2.	Explain in Detail about Design of Control Unit.	[10M]
	OR	
3.	Explain in Detail about Timing and Control of Computer organization with neat sketch.	[10M]
4.	Explain in detail about Machine language instruction format in 8086.	[10M]
	OR	
5.	Explain in detail about Assembler Directives in 8086.	[10M]
6.	Explain Timings and delays of 8086.	[10M]
	OR	
7.	Explain in Detail about Macros in 8086.	[10M]
8.	Explain Asynchronous data transfer in 8086.	[10M]
	OR	
9.	Explain Direct Memory Access (DMA) for 8086.	[10M]
10.	Differentiate between Memory Hierarchy and Main Memory.	[10M]
	OR	
11.	Explain in Detail about RISC Pipeline.	[10M]

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