Code No.: EC502PC

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CMR ENGINEERING COLLEGE: : HYDERABAD UGC AUTONOMOUS

III-B.TECH-I-Semester End Examinations (Supply) - June- 2025 MICROPROCESSORS & MICROCONTROLLERS (ECE)

[Time: 3 Hours] [Max. Marks: 70]

Note: This question paper contains two parts A and B.

Part A is compulsory which carries 20 marks. Answer all questions in Part A.

Part B consists of 5 Units. Answer any one full question from each unit. Each question carries 10 marks.

| | <u>PART-A</u> | (20 Marks) |
|-------------------------------|---|--|
| 1. a) b) c) d) e) f) g) h) i) | How to calculate the Physical memory of 8086, with one example? Draw the flag register bits in 8086 microprocessor. List out the features of 8051 Microcontroller. What are the different addressing modes used in 8051 microcontroller? Write short notes on USB. Give the RS 232 Standard details. List out different 32-bit registers used in ARM processor. List out few comparisons of ARM and 8051 Microcontroller. Explain the different applications of OMAP processor. Expand OMAP processor and its memory capacity. | [2M] [2M] [2M] [2M] [2M] [2M] [2M] [2M] |
| 2. | What is memory segmentation? Explain the use of segmentation in 8 microprocessor. | (50 Marks) (086 [10M] |
| 3. | OR List out different string manipulation instructions used in 8086 microprocessor explain each one in detail. | and [10M] |
| 4. | What are the interrupts available in 8051? Explain about the Interrupt Structure. OR | [10M] |
| 5. | Draw and explain the internal architecture of 8051 family microcontroller and expeach block of it. | lain [10M] |
| 6. | Draw the internal circuit diagram of UART and explain the function of each bloc detail. | k in [10M] |
| 7. | OR Draw the interface circuit diagram of LCD with 8051 and explain its operation detail. | n in [10M] |
| 8. | Define Pipeline? Explain the Five stage pipeline concept in ARM processor. OR | [10M] |
| 9. | Explain the concept of instruction set of ARM in detail. | [10M] |
| 10. | Explain the concept of super scalar pipeline of CORTEX processor along with cirdiagram. | cuit [10M] |
| 11 | OR Evaloin OMAP processor evaluations and evaloin each one type in detail | [10] /[] |
| 11. | Explain OMAP processor architecture and explain each one type in detail. | [10M] |