Code No: 133AB

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD B.Tech II Year I Semester Examinations, April/May - 2018

ANALOG ELECTRONICS

(Electronics and Communication Engineering)

Max. Marks: 75 Time: 3 Hours

Note: This question paper contains two parts A and B.

Part A is compulsory which carries 25 marks. Answer all questions in Part A.

Part B consists of 5 Units. Answer any one full question from each unit.

Each question carries 10 marks and may have a, b, c as sub questions.

PART- A

1	.a)	Write the characteristic of CE amplifier.	[2]			
1		write the characteristic transfer agreement to the characteristic transfer agreement transfer agreement to the characteristic transfer agreement transfer	[3]			
	b)	Discuss the need of Darlington pair circuit.	L 3			
	c)	What is meant by gain handwidth product?	[2]			
	d)	Short circuit CE current gain of a transistor is 25 at a frequency of 2 MHz. I				
i,	uj	calculate f_T , h_{fe} and $ A_i $ at frequency of 10 MHz and 100 MHz.	[3]			
Ż		Calculate 17, The and Pari at Trequency of	[2]			
	e)	State the advantages and disadvantages of cascode stage.	[2]			
	f)	What is the folded cascode amplifier? Discuss.	[3]			
	a)	Classify the various negative feedback amplifers.	[2]			
	g)	For a phase shift oscillator, the feedback network uses R=6 K Ω and C=1500 pF. The				
	h)	For a phase shift oscillator, the recubility network are strong of the KO. Calculate the	a fraguency			
		transistorized amplifier used, has a collector resistance of 18 K Ω . Calculate th	e frequency			
		of oscillation and minimum value of he of the transistor.	[3]			

of oscillation and minimum value of hie of the [2] What is the use of the heat sink in power amplifiers?

131 What is meant by loaded and unloaded Q.

PART-B

(50 Marks)

(25 Marks)

Discuss the low frequency response of BJT amplifier and the effect of coupling and 2.a) bypass capacitors.

Explain the different coupling schemes used in amplifiers.

[5+5]

For a two stage amplifier shown in figure 1 calculate (a)Av (b) Avs. (c) Ri (d) Ro Neglect the effect of all capacitances, Assume hat the both the transistors are identical with following parameters. h_{fe} =50, h_{ie} =1.1K Ω , h_{re} =2.5 ×10⁻⁴ h_{oe} =24×10⁻⁶ A/V.

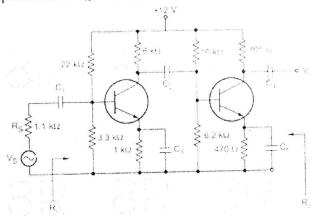
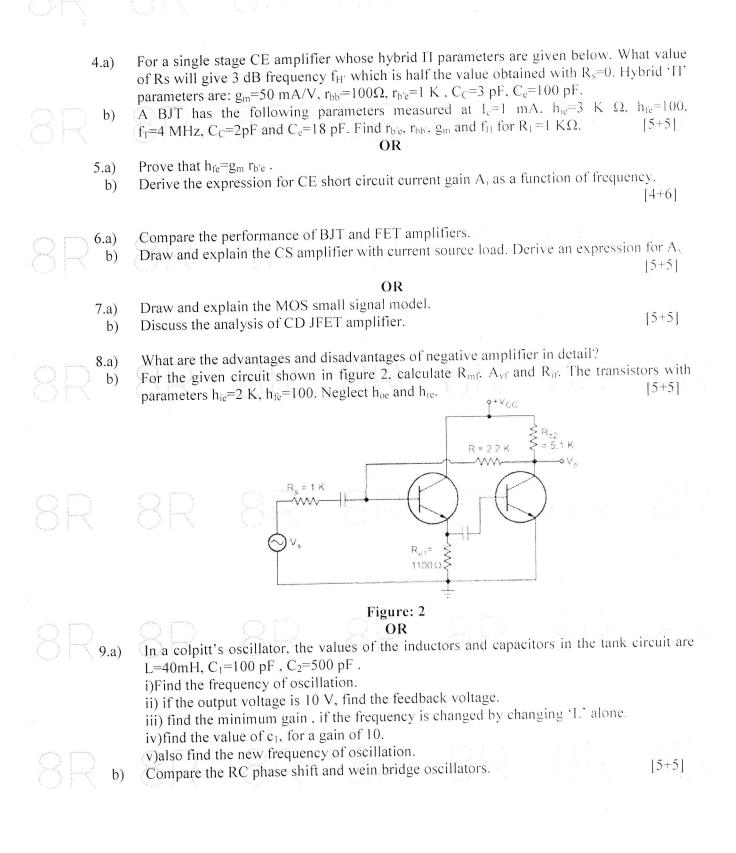


Figure: 1



3H,	8K			<i>2</i> 1			
10.a) b)	Design a class B power amplifier to deliver 25 W to a load resistors $R_1 - \delta \Omega$ using transformer coupling, $V_m = V_{CC} = 25 \text{ V}$. Assume reasonable data where ever necessary. [5+5] OR						
8R **	Draw and ex	plain the double	tuned amplifier w	ith the help of tr	e frequency resp.	[5+5]	
8R	8R						
8R							
3R	8 1						
3 R		8R					