

Code No: C5705

**JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M.TECH I SEMESTER EXAMINATIONS APRIL/MAY-2012
HARDWARE SOFTWARE CO-DESIGN
(VLSI SYSTEM DESIGN)**

Time: 3hours**Max.Marks:60**

**Answer any five questions
All questions carry equal marks**

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- 1.a) Differentiate static and dynamic partitioning techniques.
- b) Explain the parallel processor architectures.

- 2.a) Give a note on architecture specialization techniques.
- b) Explain shared memory communication in co-design.

3. Write a note on the development needs of embedded software in co-design.

4. What is concurrent computation? Explain the characterization of coordination paradigms

5. Discuss in detail the compilation techniques for embedded processor architectures.

6. Give the explanation about Target architectures in detail.

7. What is Hardware Software partitioning? Explain the Lycos system.

8. Write a short on the following:
 - a) Concurrency
 - b) Simulation
 - c) Prototyping.

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