R13 Code No: 117CZ

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD B. Tech IV Year I Semester Examinations, November/December - 2017 EMBEDDED SYSTEM DESIGN

T:	(Common to ECE, ETM)								
	3 Hours	Max. Mar	·ks: 75						
Note:	This question paper contains two parts A and B.	· D ·	D						
	Part A is compulsory which carries 25 marks. Answer all questions in Part A. Part B consists of 5 Units. Answer any one full question from each unit. Each question								
	carries 10 marks and may have a, b, c as sub questions.								
ette parae,	ouries to marks and may have a, o, e as sub questions.	Market State of	adition at the term						
3 H	SH SH SPART-A SH	SH	(25 Marks)						
1.a)	List the characteristics of an embedded system.		[2]						
b)	What is the difference between a system and an embedded system? [3]								
c)	What is actuator?		[2]						
d)	What are the considerations for processor selection?		[3]						
e)	Explain the role of reset circuit in an embedded system.		[2]	2					
$\begin{array}{c} f \\ g \end{array}$	What is the difference between real time clock and watchdog time When do you use cooperative scheduling?	· 25	[3] [2]	i i					
h)	What is the function of timer in RTOS?	The state of the s	[3]	1.0					
i)	What is Remote Procedure Call and explain its working?		[2]						
j)	What is meant by concurrency of task execution in real time system	m?	[3]						
			[-1						
3 R	8R 8R 8R 8R	88	(50 Marks)						
2.a)	Explain the major application areas of embedded systems.								
b)	What are the components of Embedded System Hardware?		[5+5]						
_	OR								
3.	Discuss the purpose of embedded systems. List the design metrics	used to comp							
The state of	an an an an	production of the same	[10]						
4	With a neat diagram, explain the architecture of a general purpose	processor	[10]						
od" i k"·	OR	processor,	[10],	No.					
5.a)	Write the difference between general purpose processors and doma	ain specific p	rocessors.						
	Discuss the aspects of memory allocation and mapping in embedded		[5+5]						
			· · · · · · · · · · · · · · · ·						
	What are the design criteria of external brown-out protection circul	it.							
) b)	How to design and implement firmware for embedded systems?		[5+5]	green.					
2 M ,									
7.	Explain with one example, how to change the bus frequency of the	processor.	[10]						

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8.a) b) 9.	How do we initiate round robin time series scheduling? How lower priority task executes in a preemptive scheduler? OR Write the basic design principles when using an RTOS to design Explain in detail the following device drivers a) Serial port device driver b) Device drivers for internal programmable timing devices. OR Explain the inter task communication offered by RTOS. Explain message-passing communication system in detail.				n of sample RTC	[545] DS.[10]	
10.					8R	[5+5] [5+ 5]	
8R	8 R	88	00000-	- 13 P	3R	8R	
8R	8R	8R	81	8R	8R	8 R	2
8R	8R	8R	8R	82	8R.	8 8	8
88	3R	8 R				3E	2
8 R	82	82	88	8R	8R	8R	<u> </u>